

Known Differences Between AP560AE-ISO-LF and Not Recommended for New Designs AP560E-ISO-LF

The AP560AE-ISO-LF was designed to replace the AP560E-ISO-LF which is no longer recommended for new designs. The reason for this replacement is a known issue with the AP560E-ISO-LF which occurs when a CAN controller interrupt line is asserted in the middle of a read transaction. The read transaction will timeout and return 0xFFs. We have fixed this problem by updating the FPGA from a Xilinx xc7a15t part to a xc7a50t part and using a more robust PCIe to AXI interface within the FPGA. This did not require a new board design, and everything outside of the FPGA remains the same. This did result in some memory map changes detailed below and will require new device drivers. Please refer to the AP560AE-ISO-LF User's Manual for more information.

PCIe Device ID and Subsystem ID

AP560E-ISO-LF:

0x7028

AP560AE-ISO-LF:

0x702C

Memory Map Changes

Please refer to the AP560AE-ISO-LF User's Manual for more detail on these changes:

Interrupt Registers
XADC System Monitor Registers
Firmware Revision Register
Location in System Register