

Acromag's re-configurable FPGA I/O modules allow you to offload CPU computing tasks for improved performance. These customizable modules feature a DSP-capable FPGA which allows you to install your own code and algorithms for high-speed signal processing functions. Our newest FPGA module includes a PowerPC core for fast processing of complex, memory-intensive computations. Acromag FPGA I/O modules can help you complete your projects on time, on budget, and with superior system performance.

[Whitepaper: A Primer on FPGA-based DSP Applications](#)

Offload CPU computing to FPGA modules

User-Configurable FPGA module has PowerPC core

Acromag's [PMC-VFX70](#) boards feature a reconfigurable Xilinx® Virtex™-5 FPGA with embedded PowerPC. The FPGA is well supported with multiple high-speed memory buffers and a high-throughput PCI-X interface. Field I/O interfaces to the FPGA via the rear J4/P4 connector and/or with optional front mezzanine plug-in I/O modules. The result is a powerful and flexible I/O processor module that is capable of executing your custom instruction sets and algorithms.



Powerful and versatile, this PMC module employs Xilinx's VFX70T Virtex-5 FPGA with 71,680 logic cells and an embedded PowerPC 440 processor 32-bit RISC core. With the configurable FPGA's hard core PowerPC, developers can customize off-the-shelf PMC modules and use high-performance parallel and serial processing to solve the most challenging computing applications.

[More info](#)

[Keeping Heat in Check: Conduction Cooling Expands Application Boundaries.](#)

For more information contact Acromag's sales department at 248-295-0310 or send an email to

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March 30 - April 3

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